Search Notes	Application/Control No.	Applicant(s)/Patent Under Reexamination
	10680444	CHEN ET AL.
	Examiner Jacob, Mary C	Art Unit 2123

Notes	Date	Examiner
Inventor Name Search: EAST, IEEE, Google Scholar	10/02/2006	MCJ
East Search: See Search History	10/02/2006	MCJ
IEEE: fpga <and> complete <and> (interconnect <or> connect); fpga <and> graph <and> complete <and> (connect <or> interconnect)	10/2/06	MCJ

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Class	SubClass	Date	Examiner
703	23	10/02/2006	MCJ

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